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PATENT

3724

CA

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Application of

Kunio KISHIMOTO, et al.

Serial No.: 09/736,499

Filed: December 15, 2000

For: METHOD OF MANUFACTURING A CIRCUIT BOARD AND ITS
MANUFACTURING APPARATUS

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INFORMATION DISCLOSURE STATEMENT

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This Information Disclosure Statement is being filed more than three months after the U.S. filing date and after the mailing date of a Final Rejection or Notice of Allowance, but before payment of the Issue Fee.

REQUEST TO CONSIDER REFERENCES AFTER CLOSE OF PROSECUTION AND
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The undersigned hereby requests consideration and entry of this Information Disclosure Statement and accompanying references under 37 CFR 1.97(d).

Please charge the processing fee under 1.17(p) of \$180.00 to Deposit Account 500417.

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CERTIFICATION PARAGRAPH

The undersigned certifies that, to the best of his knowledge, each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement as described in 37 CFR 1.97(e)(1).

Each non-English language reference was cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the foreign search report or office action, together with an English language version thereof, is attached for the Examiner's information.

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Respectfully submitted,

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